

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. PA000400	SERIAL NO. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANT Sindhushayana, et al.	
				FILING DATE Herewith	GROUP Unknown
DATE MAILED: 9/25/01					

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09/25/01

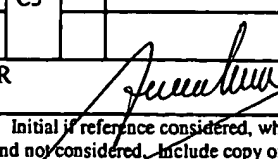
U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
JAT	A1 6 2 9 2 9 1 8	9/18/01	Sindhushayana, et al.	714	755	11/5/98
JAT	A2 6 1 8 2 2 6 1	1/30/01	Haller, et al.	714	758	7/9/99
JAT	A3 5 9 3 3 4 6 2	8/3/99	Viterbi, et al.	375	341	11/6/96
JAT	A4 5 4 4 2 6 2 7	8/15/95	Viterbi, et al.	370	22	6/24/93
	A5					
	A6					
	A7					
	A8					

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						
	B3						
	B4						
	B5						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

JAT	C1	Chung, et al. "Analysis of Sum-Product Decoding of Low-Density Parity-Check Codes Using a Gaussian Approximation" IEEE Transactions on Information Theory 47(2): 657-670 (2001).
	C2	
	C3	
EXAMINER		DATE CONSIDERED
		12-09-2004

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) DATE MAILED: December 10, 2003	ATTY. DOCKET NO.	APPLICATION NO.
	000400	09/965,518
	APPLICANT Sindhushayana et al.	
	FILING DATE	GROUP
	09/25/2001	2631

U.S. PATENT DOCUMENTS

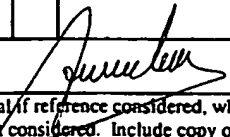
EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1						
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	B1						
	B2						
	B3						
	B4						
	B5						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

JAT	C1	Worm et al. "A HIGH-SPEED MAP ARCHITECTURE WITH OPTIMIZED MEMORY SIZE AND POWER CONSUMPTION," 2000 IEEE Workshop on signal processing systems. SIPS 2000, October 11, 2000, pages 265-274.
JAT	C2	Pietrobon. "IMPLEMENTATION AND PERFORMANCE OF A TURBO/MAP DECODER," INTERNATIONAL JOURNAL OF SATELLITE COMMUNICATIONS, John Wiley & Sons, Ltd., U.S. vol. 16, no. 1, 1998, pages 23-46.
	C3	
	C4	
	C4	
	C5	
EXAMINER 		DATE CONSIDERED 12/09/2004
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